

(2) Please rewrite claim 2 as follows:

1 2. (Amended) A circuit comprising:
2 a. an input,
3 b. an output,
4 c. a chopper stabilized, multistage feedforward amplifier connected between said input
5 and said output, and
6 d. an analog to digital converter connected between said amplifier and said output.

(3) Please rewrite claim 11 as follows:

1 11. (Amended) The circuit of claim 2, further comprising a plurality of integrators connected
2 between said amplifier and said output.

(4) Please rewrite claim 12 as follows:

1 12. (Amended) The circuit of claim 2 fabricated on an integrated circuit.

(5) Please cancel claim 28.

(6) Please rewrite claim 29 as follows:

1 29. (Amended) A method of designing an integrated circuit comprising the steps of:
2 specifying an input, an output and a chopper stabilized, multistage, feedforward amplifier
3 connected between said input and said output; and
4 specifying a delta sigma modulator to be connected between said amplifier and said output.

(7) Please cancel claim 30.

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(8) Please rewrite claim 31 as follows:

1 31. (Amended) A method of fabricating an integrated circuit comprising the steps of:
2 providing an input, an output and a chopper stabilized, multistage, feedforward amplifier
3 connected between said input and said output; and
4 providing a delta sigma modulator to be connected between said amplifier and said output.

Respectfully submitted,

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